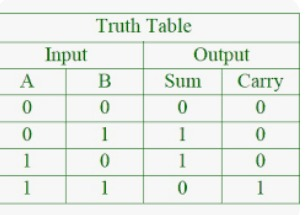
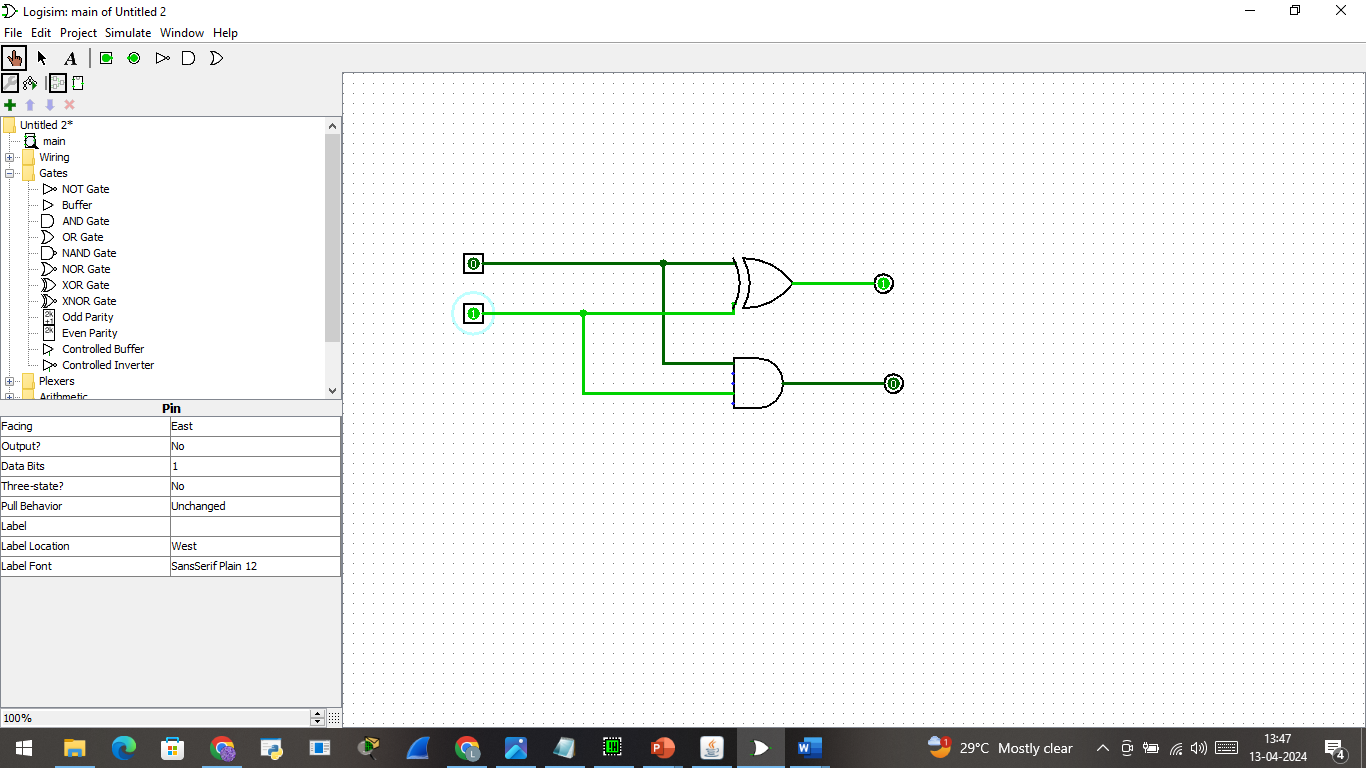
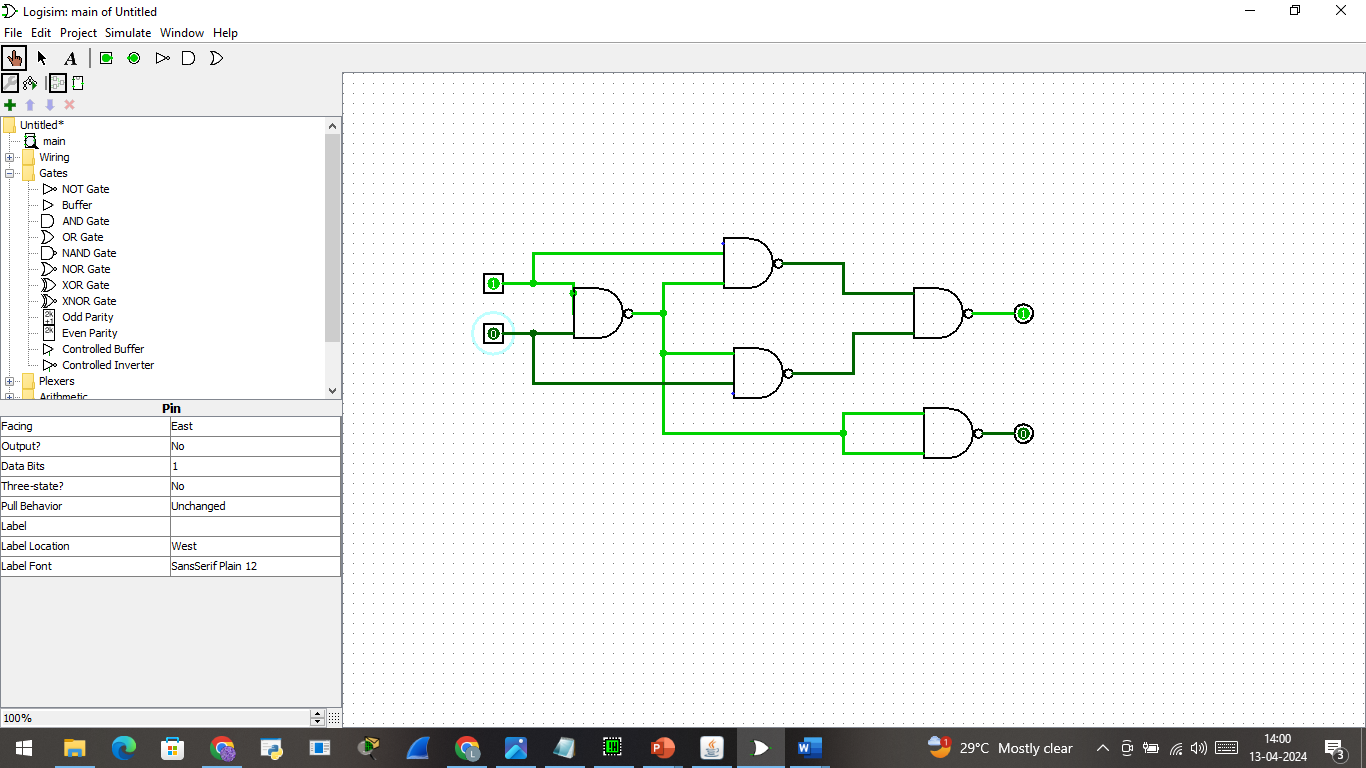
**Half adder**

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**Half adder- using normal gates**



**Half adder- using NAND gates**



**Half adder- using NOR gates**

